

# Eduardo MÉNDEZ

Embedded SW Engineer  
Telecommunication Engineer  
PMP



Born at 19/05/1984 in Seville  
Living in Munich

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## Professional experience

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### September 2018 – Today EGNOS V3 SW Engineer - Airbus DS (Munich)

External worker from Deimos Space to support the EGNOS project at Airbus facilities in Ottobrunn (Munich area). Main activities:

- Support to design, implementation and validation of SW Algorithmic Functions (AFs)
- Performance validation of AF models (including run-time)
- Software acceptance / testing and verification. MISRA-C standard is applied in these tasks.
- Requirements management

Strategic project for the EU.

### February 2017 – August 2018 Embedded SW Manager - RBZ (Madrid)

Head of the Embedded SW department at RBZ. Responsible for supervising, managing and motivating team members on a daily basis. In addition, I was in charge of distributing the project tasks, setting and discussing deadlines for the internal team and agreeing with customers the different technical aspects.

### March 2010 – August 2018 Embedded SW Engineer - RBZ (Madrid)

RBZ is a little company specialized in fast design of industrial prototypes. I have been working on very different projects related to standalone applications, RTOS, embedded Linux or FPGA designs, to be mostly used in the railway, military or aerospace industries, but also in IoT devices. The complexity and length of these projects is very heterogeneous: from 3 to 18 months, at any level of embedded SW design. All projects under tight deadlines constraints. Very brief description of several projects where I have been working:

- Complete design of an ADS-B Mode-S receiver for ATC (Air Traffic Control) applications based entirely on a FPGA design
- Design of a highly adaptive tester to verify satellite payloads
- SW design for a power distribution box for heavy military vehicles
- Critical SW design for a railway signaling system (SIL-4)

Progressively specialized in safety and critical applications.

### July 2009 – March 2010 Firmware Responsible - Altatec Sistemas (Madrid)

Responsible for firmware development in the design of a new domestic security system of an important international security company. This project involved the software of four different wireless devices, dealing with low power constraints and a timeline very adjusted.

Complete technical about the project: electronic, firmware, radio interface, communication protocols, GUI, etc.

## July 2008 – March 2009 Consultant at Pace - Alten (Paris)

Consultant in Pace working in the design and implementation of a Web Information System to be used in interoperability and testing events. The purpose of this project was to accelerate the testing phase of new products.

Project with certain complexity and a very adjusted timeline.

## February 2008 – June 2008 CITI-INRIA (Lyon, France)

Internship to write my studies final project developed in the laboratory CITI-INRIA (Lyon), in collaboration with France Telecom. Project title: "Implementation FPGA/DSP of an multi-array multi-mode receiver based on Software Defined Radio (SDR)". This project was developed by using two digital boards, each of them with one FPGA and four DSPs.

## Education

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### 2016 – 2016 EOI: Escuela de Negocios

- Executive Project Management Program (PMP)
- PMP obtained in December 2016

### 2007 – 2008 INSA Lyon

- Erasmus scholarship in Lyon (France)

### 2002 – 2008 Telecommunication Engineer

- Escuela Superior de Ingenieros de Sevilla
- Specialized in Electronic for Communications

## Languages

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<b>Spanish</b>	Mother tongue
<b>English</b>	High level
<b>French</b>	High level
<b>German</b>	Basic level. Currently taking classes.

## Background

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### September, 2018 – Udacity Deep Learning Nanodegree

4 Months course to get basic knowledge in Artificial Intelligence and related technologies: Keras, Tensorflow, Pytorch.

### October, 2010 – European Satellite Navigation Competition

I reached the third regional prize in the region of Madrid in the European Satellite Navigation Competition 2010 with the idea: "GNSS based encryption system".

### February, 2006 – Bocconi Economix Workshop - Milan

I was invited by the Università Bocconi to attend as finalist to the final of their contest on economy and business administration.

## Personal skills

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- Good experience in team working and in project coordination.
- Practical vision
- Ability to work in group or autonomously
- Experience working in international environments
- Used to work under stress

## Technical skills

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Summary of the most representative skills:

### **Programming languages**

C/C++, Python, ADA, .NET, Java, Assembler, Visual Basic, HTML, XML, ASP, PHP. GCC Compilers.

### **Development environments**

Eclipse and related (CodeWarrior, CodeComposer, VisualDSP++, SoftConsole, ...) Xilinx ISE, Libero, Ride, Signum, Lauterbach Trace32 and Ronetix tools. No problems using oscilloscopes, logic analyzers, function generators, etc.

### **Hardware / Embedded systems**

ARM Processors. Xilinx and Altera FPGAs.

Very experienced in FreeRTOS. Good knowledge of MQX, Unison RTOS.

Linux Embedded: Bootstrap, U-Boot, kernel, drivers and file system.

Experience in hardware and BSP testing support.

### **Protocols**

Wifi, xDSL, RTP, RDSI, GSM, GPRS, DVB, TCP-IP, UDP, OSI, Bluetooth.

Buses: SPI, I2C, I2S, Ethernet, CAN, CANOpen, J1939, VISA.

### **Standards**

EN-50125, DO178C, MISRA-C, ECSS.

Experience in system/software requirements. DOORS.

### **Project Management**

PMP certification.

Scrum.

### **Others**

VHDL, Matlab, Maple, Simulink, LabView, Spice, UML.

Some experience in Altium.